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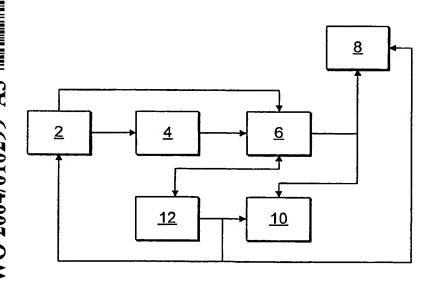
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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: SELF-TEST SYSTEM



(57) Abstract: An electronic system comprising a system to be monitored (2) and a plurality of fault-monitoring systems (4, 6) each of which is adapted to output a fault signal when an input indicates that the electronic system is in a fault condition associated with the fault-monitoring system. fault-monitoring systems are arranged in a cascade fashion such that a fault signal output from one fault-monitoring system (4) is provided as an input to a subsequent fault-monitoring system (6) in the cascade of fault-monitoring systems to simulate a fault condition associated with the subsequent fault-monitoring system. The output of the final fault-monitoring system in the cascade gives an indication of whether there is a fault with any of the fault-monitoring systems.

INTERNATIONAL SEARCH REPORT

	GB 03/03141
a. classification of subject matter IPC 7 G06F11/267	
According to International Patent Classification (IPC) or to both national classification	n and IPC
Minimum documentation searched (classification system followed by classification s IPC 7 G06F	symbols)
Documentation searched other than minimum documentation to the extent that such	documents are included in the fields searched .
Electronic data base consulted during the international search (name of data base a EPO-Internal, WPI Data, INSPEC	and, where practical, search terms used)
C. DOCUMENTS CONSIDERED TO BE RELEVANT	
Category ° Citation of document, with Indication, where appropriate, of the releva	nt passages Relevant to claim No.
X EP 0 467 719 A (HONEYWELL INC) 22 January 1992 (1992-01-22)	1,2,6,8, 10,11, 15,17
column 2, line 35 - column 4, line figure 2	
X US 4 586 179 A (SIRAZI SEMIR ET A 29 April 1986 (1986-04-29)	1,2,6,8, 10,11, 15,17
column 3, line 35 - line 56 column 5, line 16 - line 65 column 7, line 7 - line 39 claim 1; figures 1,2	
-/	
X Further documents are listed in the continuation of box C.	X Patent family members are listed in annex.
° Special categories of cited documents : "A" document defining the general state of the art which is not considered to be of particular relevance.	later document published after the International filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
filing date "L" document which may throw doubts on priority_claim(s) or	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone document of particular relevance; the claimed invention
citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but	cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
later than the priority date claimed "&" Date of the actual completion of the international search	" document member of the same patent family Date of mailing of the international search report
19 May 2004	09.08.2004
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentiaan 2	Authorized officer
NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl Fax: (+31-70) 340-3016	Gorzewski, M



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Category* Citation of document, with indication, where appropriate, of the relevant passages A MCCLUSKEY E J: "DESIGN TECHNIQUES FOR TESTABLE EMBEDDED ERROR CHECKERS" COMPUTER, IEEE COMPUTER SOCIETY, LONG BEACH., CA, US, US, vol. 23, no. 7, 1 July 1990 (1990-07-01), pages 84-88, XP000137688 ISSN: 0018-9162 page 86, middle column, line 1 - right-hand column, line 32; figures 2B,4	Relevant to claim No.
A MCCLUSKEY E J: "DESIGN TECHNIQUES FOR TESTABLE EMBEDDED ERROR CHECKERS" COMPUTER, IEEE COMPUTER SOCIETY, LONG BEACH., CA, US, US, vol. 23, no. 7, 1 July 1990 (1990-07-01), pages 84-88, XP000137688 ISSN: 0018-9162 page 86, middle column, line 1 -	1,10
TESTABLE EMBEDDED ERROR CHECKERS" COMPUTER, IEEE COMPUTER SOCIETY, LONG BEACH., CA, US, US, vol. 23, no. 7, 1 July 1990 (1990-07-01), pages 84-88, XP000137688 ISSN: 0018-9162 page 86, middle column, line 1 -	
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Box I Observations where certain claims were found unsearchable (Continuation of item 1 of first sheet)				
This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:				
1. Claims Nos.: because they relate to subject matter not required to be searched by this Authority, namely:				
Claims Nos.: because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically:				
3. Claims Nos.: because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).				
Box II Observations where unity of invention is lacking (Continuation of Item 2 of first sheet)				
This International Searching Authority found multiple inventions in this international application, as follows:				
see additional sheet				
As all required additional search fees were timely paid by the applicant, this international Search Report covers all searchable claims.				
2. As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.				
3. As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:				
4. X No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.: 1-18				
Remark on Protest The additional search fees were accompanied by the applicant's protest. No protest accompanied the payment of additional search fees.				

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-18

An electronic system comprising a plurality of fault-monitoring systems, which are arranged in a cascaded fashion such that the fault signal output from one of the fault-monitoring systems is provided as an input to the subsequent fault-monitoring system.

2. claims: 19-31

A system comprising at least one fault-monitoring system, the system being placed in a fault condition, the fault signal generated by the fault monitoring system being stored in a non-volatile memory.



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Patent document cited in search report		Publication date		Patent family member(s)	Publication - date
EP 0467719	Α .	22-01-1992	US EP	5151854 A 0467719 A2	29-09-1992 22-01-1992
US 4586179	Α .	29-04-1986	NONE		